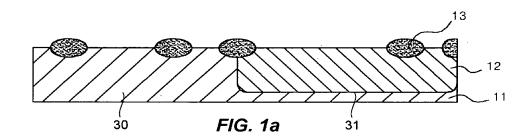
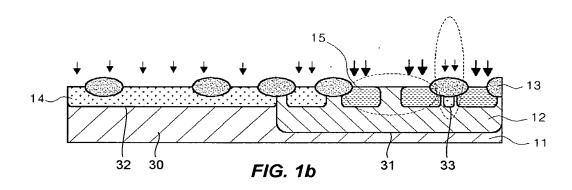
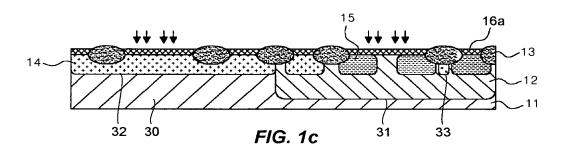
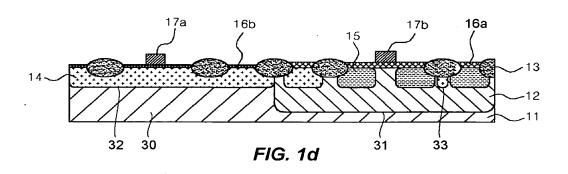
Serial No. 10/762,818 - Filed 1/22/04 Inventor: Seong-Wook Kim Method for Fabricating Merged Logics CMOS Device Group Art Unit: 2818 - Examiner: Phuc T. Dang Replacement Sheet 1 of 2











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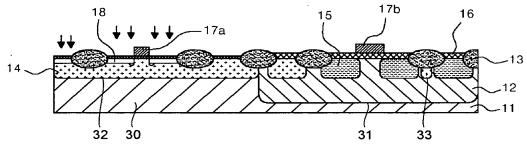


FIG. 1e

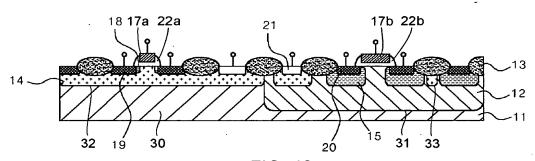


FIG. 1f